



# INFORMATION DISCLOSURE CITATION

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APPLICANT  
Shailender Chaudhry, et al.

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## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

## FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						Yes No
PMM	WO 01 93028 A2	6 December 2001	PCT			
PMM	WO 03/054693	3 July 2003	PCT			

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

PMM	"The Potential for Using Thread-Level Data Speculation to Facilitate Automatic Parallelization", by J. Gregory Steffan et al., 1998, IEEE, Pages 2-13.

EXAMINER <u>/Patrick M. Moore/ (10/01/2006)</u>	DATE CONSIDERED <u>10/01/2006</u>
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